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**\*\* CONTINUING DATA \*\*\*\*\***  
 This application is a 371 of PCT/JP04/12904 08/31/2004 LC YES

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***  
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**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***  
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Foreign Priority claimed 35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance LC	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWINGS</b> 4	<b>TOTAL CLAIMS</b> 4	<b>INDEPENDENT CLAIMS</b> 1
Verified and	/LATANYA N CRAWFORD/ Examiner's signature	Initials				

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**TITLE**  
 Semiconductor integrated circuit

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